

VinChip Systems announces USB PHY Verification Services

San Jose, CA. April 9,2007 -VinChip Systems one of the leaders in USB IP licensing announced immediate availability of PHY Verification Services to address the growing need to validate USB PHY before silicon and post tape out

Home grown or third party USB PHY (UTMI/UTMI+/ULPI) can be tested with VinChip Verification IP containing silicon proven VinChip RTL and test environment to identify bugs early in the design . Customers can get full access to VinChip RTL and full test case database as a part of this service. Post silicon customer's PHY will be tested in VinChip FPGA emulation environment and checked for USB – IF compliance

“Our engagement with early Beta customers helped us to validate our VIP with third party PHY model” commented Murugesan Jeyachandran, VP Engineering VinChip Systems. “VinChip today has successfully interoperated its IP with more than 10 different PHY offerings that are available in the market today” he added

VinChip Systems USB Intellectual Property is being used by more than 60 customers worldwide today. Deliverables include Verilog or VHDL models along with directed and random test bench verification environment. For more information on this service please contact info@vinchip.com